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|-------------|---|-------------|-------------------|
| Patentee: | Yeo, et al. | Docket No.: | TSM03-0421 |
| Serial No.: | 10/608,287 | Patent No.: | 7,005,330 B2 |
| Date Filed: | June 27, 2003 | Issued: | February 28, 2006 |
| Title: | Structure and Method for Forming the Gate Electrode in a Multiple-Gate Transistor | | |

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| Patent Number | 7,005,330 B2 |
| Issue Date | 02-28-06 |
| Application Number | 10/608,287 |
| Filing Date | 06-27-03 |
| First Named Inventor | Yeo |
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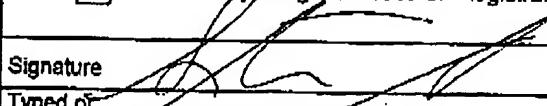
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Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

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